

10/049486

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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10049486	FILING DATE 02/28/2002	CLASS 251	SUBCLASS 604	GAU 2811	EXAMINER <u>OWENS</u>
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## \*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A 371 OF PCT/CN00/00068 03/29/2000

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## \*\* FOREIGN APPLICATIONS VERIFIED:

CHINA 99118929.9 08/30/1999

PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	<input type="checkbox"/> RESCIND
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
ATTORNEY DOCKET NO FP01074US		
TITLE : Parallel plate diode		

U.S.DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		DRAWING	
		Sheets Drwg.	Figs.Drwg.
		Print Fig.	
		Primary Examiner	
		PREPARED FOR ISSUE	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
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